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First Named Inventor	Frankie F. Roohparvar
al No.	10/785,556
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Group Art Unit	2818
Examiner Name	Ly D. Pham
Confirmation No.	8138
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TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING REJECTION OVER A PRIOR ART PATENT

Title: FLASH MEMORY WITH RDRAM INTERFACE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

The owner, Micron Technology, Inc., of 100% percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. §§ 154 to 156 and 173, as presently shortened by any terminal disclaimer, of co-pending patent application, U.S. Serial Number 09/943399, (now U. S. Patent No. 6741497 issued May 25, 2004). The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer, the owner does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. §§ 154 to 156 and 173 of the prior patent, as presently shortened by any terminal disclaimer, in the event that it later: expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 37 CFR § 1.321, has all claims canceled by a reexamination certificate, is reissued, or is in any manner terminated prior to the

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Please charge any additional fees or credit any overpayment to Deposit Account No. 501373.

Respectfully submitted,

Date: 9/ 7/0

Kenneth W. Bolvin Reg. No. 34,125

Attorneys for Applicant Leffert Jay & Polglaze P.O. Box 581009 Minneapolis, MN 55458-1009 T 612 312-2200 F 612 312-2250